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(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
18 January 2001 (18.01.2001)

PCT

(10) International Publication Number  
**WO 01/04944 A2**

(51) International Patent Classification<sup>7</sup>: H01L 21/321,  
B08B 3/10, H01L 21/324, 21/00

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(21) International Application Number: PCT/US00/40354

(22) International Filing Date: 11 July 2000 (11.07.2000)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
09/351,868 12 July 1999 (12.07.1999) US

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,  
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ,  
DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR,  
HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR,  
LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ,  
NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM,  
TR, TT, UA, UG, UZ, VN, YU, ZA, ZW.

(84) Designated States (*regional*): ARIPO patent (GH, GM,  
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian  
patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European  
patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE,  
IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG,  
CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

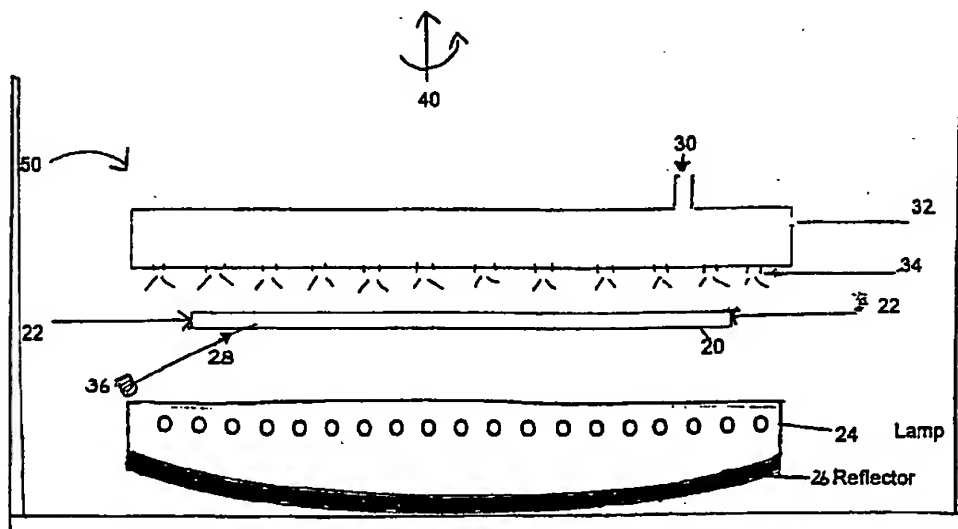
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Published:  
— Without international search report and to be republished  
upon receipt of that report.

[Continued on next page]

(54) Title: METHOD AND APPARATUS FOR SIMULTANEOUSLY CLEANING AND ANNEALING A WORKPIECE



(57) Abstract: This invention is directed to a method and apparatus for simultaneously cleaning and annealing a plated semiconductor workpiece. A chamber for simultaneously cleaning and annealing a semiconductor workpiece is provided herein. The method according to the present invention includes the steps rinsing the workpiece with mineral acids and DI water, then simultaneously cleaning/annealing the workpiece with heated deoxygenated DI water. By this present method, tool cross contamination by way of plated metal is greatly reduced, and also grain recovery and grain growth at room temperature is greatly accelerated. Thus, the annealing time of grain growth is dramatically reduced using the present method and apparatus.

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## METHOD AND APPARATUS FOR SIMULTANEOUSLY CLEANING AND ANNEALING A WORKPIECE

### FIELD OF THE INVENTION

5       The present invention relates to a method and apparatus for simultaneously cleaning and annealing a conductive film deposited on a substrate, and more particularly, to a method and apparatus for effectively rinsing contaminants from the front and back surfaces of a workpiece while annealing the same. This is accomplished by using high temperature de-ionized oxygen free water or other suitable liquids for cleaning and annealing. By this present method, tool cross contamination by way of plated metal is greatly reduced, and also grain recovery and grain growth at room temperature is greatly accelerated. Thus, the annealing time of grain growth is dramatically reduced using the present method and apparatus.

### BACKGROUND OF THE INVENTION

15       The semiconductor industry continues to be a multi-billion dollar industry as millions of semiconductor chips, devices, and integrated circuits are produced in fabrication centers throughout the world. As the demand for semiconductor chips increases so does the demand for faster, less expensive, and more reliable chips.

20       There are many fabrication steps in the manufacturing of semiconductor chips. The present invention focuses on a certain phase of this manufacturing process. For example, Fig. 1 illustrates a flow chart of a phase in a conventional fabrication process. Although only a limited number of process steps are illustrated in Fig. 1, other processing steps as known in the art are used during the entire fabrication process. For instance, in the electro-deposition of copper for chip interconnect or packaging applications, after metal deposition from a suitable electroplating bath, the workpiece is rinsed thoroughly with suitable liquids to reduce the amount of metal that may be present on the workpiece backside. Further, for optimal performance, it is preferable to anneal the workpiece at room temperature, oven or furnace before the chemical mechanical polishing (CMP) step.

25       As an example, in copper chip and packaging applications, after the plating step 2, a cleaning/rinsing step 4 is typically performed to remove spurious contaminants from the substrate or workpiece backside. Contaminants typically fall into several categories: particulate; and organic and inorganic residues such as copper sulfates or other potential contaminants that may originate from the plating bath. Having a clean workpiece backside is essential in preventing copper contamination of subsequent tools.

35       The cleaning/rinsing step 4 is generally performed using a liquid chemical such as about 2 to 20% mineral acid solutions containing, for example, 5% sulfuric acid. After the workpiece backside cleaning, the mineral acid is rinsed off the workpiece using strayed de-ionized (DI) water, thus completing the cleaning/rinsing step 4.

Typically, after metal plating step 2 and cleaning/rinsing step 4, the substrate is then planarized during a CMP step 8. However, for optimal device or interconnect performance, it is desirable to anneal the workpiece to stabilize the structure of the electro-deposit in an annealing step 6. Conventionally, the workpiece can be annealed in room temperature for 2-3 days, or can be  
5 annealed in a tube furnace or conventional oven for 3 minutes to 3 hours at a temperature that ranges from 50° to 450 °C in an inert ambient like nitrogen or vacuum.

As can be appreciated, the time and effort needed during this phase of the fabrication process can be improved and simplified. Accordingly, a more efficient and time saving method and apparatus for in situ cleaning and annealing of a plated workpiece is disclosed herein.

10

## SUMMARY OF THE INVENTION

It is an object of the invention to provide a method and apparatus for in situ simultaneously cleaning/annealing of a plated substrate.

It is another object of the invention to provide a method and apparatus for simultaneously  
15 cleaning/annealing a plated semiconductor device or package.

It is a further object of the invention to provide a method and apparatus for simultaneously cleaning/annealing a semiconductor device or package with minimal time.

These and other objects of the present invention are obtained by providing a chamber for simultaneously cleaning and annealing semiconductor workpieces. The method according to the  
20 present invention includes the steps of initially rinsing (spraying) the workpiece with preferably, mineral acids such as 5-20 % sulfuric acid, followed by a brief DI water rinse, then replacing the DI water with heated deoxygenated DI water in order to simultaneously clean/anneal the workpiece. Alternatively, the workpiece can be initially rinsed with DI water and then rinsed with mineral acids. Thereafter, heated deoxygenated DI water is used to simultaneously clean/anneal the workpiece.

## 25 BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects and advantages of the invention will become apparent and more readily appreciated from the following detailed description of the presently preferred exemplary embodiment of the invention taken in conjunction with the accompanying drawings, of which:

Fig. 1 illustrates a flow chart of a limited number of steps in a conventional fabrication process;

30 Fig. 2 illustrates a flow chart of a limited number of steps in accordance with the preferred embodiment of the invention;

Fig. 3 illustrates a cross sectional view of a preferred embodiment of the invention; and

Fig. 4 illustrates a simplified block diagram of one embodiment of an overall system of the invention.

35

**DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS**

The preferred embodiments of the invention will now be described with reference to Figs. 2-4, wherein like components are designated by like reference numerals throughout the various figures.

Fig. 2 illustrates a flow chart of a limited number of processing steps in accordance with the preferred embodiment of the present invention. As illustrated, the flow chart includes a plating step 12, a cleaning/annealing step 14, and a CMP step 16. Unlike the conventional plating step 2, cleaning/rinsing step 4, annealing step 6, and CMP step 8 described in the background section of this disclosure, the present invention includes a combined simultaneous cleaning/annealing step 14, which is described in more detail later herein. Thus, with the present invention, a method and apparatus for saving time and equipment costs are disclosed.

The invention can be used with any conductive material such as copper, iron, chromium, indium, lead, lead-tin alloys, silver, tin and tin alloys, zinc, cadmium, titanium, cadmium-titanium alloys, molybdenum, ruthenium, gold, nickel, palladium, cobalt, rhodium, platinum, and various combinations thereof, and for use in the fabrication of integrated circuits, magnetic films such as permalloy, and metal seed layers having LSI to beyond ULSI integrated circuit features and dimensions. Furthermore, although a semiconductor wafer will be used to describe the preferred embodiments of the present invention, other semiconductor workpieces such as a flat panel, magnetic film head, or packaging substrate including lead-tin solder alloys, or lead free solderable alloys, may be processed using the present invention. Further, specific processing parameters such as time, temperature, pressure and the like are provided herein, which specific parameters are intended to be explanatory rather than limiting.

Consider the electro-deposition of copper from a suitable copper plating baths on a wafer. Typically, after the metal deposition, the substrate is rinsed or cleaned with copious amount of DI water and perhaps with dilute mineral acids to remove spurious copper on the wafer backside, prior to metal polish by CMP. For optimum interconnect performance, it is highly desirable to stabilize the structure of the electroplated metal by annealing the plated copper. Annealing may be allowed to occur at room temperature for over a period of three days or longer. On the other hand, annealing the plated wafer at higher temperatures may accelerate the grain structure stabilization. Thus, the plated wafer may be transported to a suitable oven or tube furnace for faster annealing. The annealing temperature may range from 60° to 450°C in an inert ambient such as nitrogen or in a reducing ambient, or even in a vacuum chamber.

Fig. 3 illustrates a cross sectional view of a preferred embodiment of the present invention. In the present invention, the inventors disclose a method for simultaneously cleaning and annealing the plated copper film in an ambient of deoxygenated DI water. For example, after copper deposition from a suitable bath, be it by electroplating or electrolyses, the plated wafer 20 is transported to a

cleaning/annealing chamber 50. In the chamber 50, the wafer 20 is supported about its edge and rotates about its major axis 40 with the aid of a support 22. Initially, DI water may be introduced into the chamber 50 via an inlet 30 and a spray type nozzle 36, and sprayed onto the rotating wafer 20 via a shower head 32 and a spray direction 28. The wafer 20 is rinsed with DI water that flows via  
5 apertures 34 to the rotating wafer 20 to create a shower type condition.

The backside of the wafer 20 is shown facing the shower head 32. In other embodiments, the plated face of the wafer 20 may face the shower head 32. In either case, a second source of DI water from the nozzle type spray 36 is sprayed on the side of the wafer 20 facing a heating lamp/source 24. Reflectors 26 are positioned on the bottom of the heating lamp/source 24. During this process, the  
10 wafer rotates between 5 to 100 RPM for a period ranging from 5 to 10 seconds or longer.

After the brief DI water rinse as described above, a dilute mineral acid solution such as 1 to 15% sulfuric acid from a closed loop recirculating bath (no shown) is introduced via the inlet 30 and nozzle type stray 36 to rinse the wafer 20 for the second time. The wafer may rotate between 5 to 100 RPM for a period of 5 to 15 seconds during this acid rinse process. The remaining solution on the  
15 wafer may then be spun off at a higher spin speed, typically between 1000 to 2000 RPM.

After the acid rinse, heated de-oxygenated DI water from a closed loop recirculating arrangement is fed through again via inlet 30 and stray nozzle 36. The wafer 20 rotation rate is then reduced to 2-20 RPM, but preferably to 5 RPM. Meanwhile, the heating lamp 24 is energized to heat the wafer 20 along with the applied deoxygenated DI water. The heated de-oxygenated DI water,  
20 which may be heated prior to being applied and also from the heating lamp 24, and the heat from the heating lamp 24 can now be used to simultaneously clean/anneal the wafer. The temperature at which DI water evaporates is 100 °C. Thus, the de-oxygenated DI water can be heated between 60-99 °C.

The temperature of the wafer may range between 60 to 450 °C, and the annealing time may range between 5 seconds to 20 minutes, although longer annealing times can be used if desired.  
25 Moreover, the higher the wafer temperature, the shorter the required anneal time. Furthermore, the heating lamp 24 can generate infrared light because infrared light does not get absorbed by de-ionized DI water, but does get absorbed by the wafer 20 during the annealing process.

In other embodiments, the wafer can be initially rinsed with mineral acid solution and then rinsed with DI water that is not heated or subjected to heat. Thereafter, deoxygenated DI water that is  
30 heated and/or subject to heat is used to simultaneously rinse/anneal the wafer.

Fig. 4 illustrates a simplified block diagram of one embodiment of an overall system of the present invention. This figure illustrates the cleaning/annealing chamber 50 that is adapted to receive DI water from a DI water source 60, acid solution from an acid solution reservoir 70, and de-oxygenated DI water from a de-oxygenated DI water reservoir 80. In addition, acid solution, and  
35 de-oxygenated DI water is adapted to flow back out of the cleaning/annealing chamber 50 into their

respective reservoirs. Any conventional components (pipes, funnels, lines, etc.) and methods for flowing these liquids into and out of the cleaning/annealing chamber 50 may be used in the invention.

Nitrogen or other suitable inert gases may be bubbled in the deoxygenated water, continuously or intermittently, to displace all or any spurious oxygen in the water. In addition, 0.5 to 50g of a pentose sugar, or other variations/additives/combinations of pentose sugars, may be added to the water to further enhance the deoxygenation process. Alternatively, hydrogen or forming gas may be bubbled into the water to impart some reducing ambient during the in situ cleaning and annealing.

Whatever type of the cleaning fluid used, the lamp induced temperature of the workpiece may be rapidly increased from around room temperature to about 450°C, for example in less than 10 seconds. The wafer anneal temperature may range from 60° to 450°C, while the anneal time may range between 5 seconds to 20 minutes, although longer annealing times can be used if desired. When the heating lamp 24 is shut off or de-energized, the annealing fluid may be allowed to continue to circulate in the chamber for an additional period, typically 5 to 20 seconds or longer before the fluid is flowed out of the chamber 50.

The wafer is then spun at a very high rotation rate for spin drying. The wafer is then unloaded from the chamber 50. Thereafter, the grain recovery and grain growth may be allowed to continue at a room temperature before metal CMP.

In accordance with the present invention, other annealing fluids such as propylene glycol and their various analogues may be used. One of the essential properties of the annealing fluid is that it does not oxidize the metal or material being annealed. Thus, it may be imperative that the fluid be deoxygenated either by bubbling an inert gas such as nitrogen, argon, or other additives that retard oxygen incorporation into the annealing fluid. The annealing fluid used in the present invention could also be a compressed heated inert gas.

In other embodiments of this invention, multiple wafers (e.g., up to 200) can be loaded into holders and annealed simultaneously. In this scenario, multiple heating lamps are used to heat the annealing fluid in the cleaning/annealing chamber while the chamber is pressurized as during the annealing process. Alternatively, infrared heating elements, inline high power heating elements, or microwave radiation, or other desirable methods can be used to heat the annealing fluid and the wafers. The annealing liquid is preferably de-oxygenated DI water or other fluids that will not oxidize the material being annealed. Further, when the chamber is pressured, the annealing fluid does not need to be circulated. However, the workpiece/wafer preferably rotates in order to optimize heat transfer within the apparatus. Other methods of heating the fluid and wafers can be used. When simultaneously annealing multiple wafers, the annealing time ranges from 10 seconds to 45 minutes, depending on the choice of annealing temperature and the amount of energy transferred from the heating source to the annealing fluid/wafer, although longer annealing times can be used if desired.



The anneal time is longer for processing multiple workpieces as opposed to process a single work piece because of a larger mass among the multiple workpieces.

To heat the annealing fluid that is held in the reservoir, cleaning/annealing chamber, or in fluid lines, a high voltage power supply may be used. Any conventional method of heating the annealing fluid in the reservoir, cleaning/annealing chamber, or in the fluid lines may be used in the present invention. The annealing fluid temperature can be controlled by a series of thermocouples in the reservoir, chamber, and return drain lines. For instance, when there is a temperature difference between the annealing fluid in the reservoir and in the chamber, the thermocouples can determine the amount of heat or power that need to be provided to the heaters in either the reservoir or chamber such that the temperature of the annealing fluid is optimal. The highest temperature to which the annealing fluid can be heated without evaporation is dependent on the fluid being used. For example, DI water will evaporate at 100 °C while propylene glycol will evaporate at about 200 °C. Thus, the propylene glycol can be heated to a temperature between 60-200 °C.

In addition, an oxygen sensor can also be used to measure the oxygen content of the annealing fluid. Inert gas bubbling into the reservoir or fluid lines towards the chamber is regulated to keep the oxygen level of the annealing fluid preferably below 3 ppms in the anneal chamber.

Heated de-oxygenated dilute mineral acid (heated above room temperature) can also be used for simultaneous wafer rinsing, cleaning, and annealing. For example, a heated solution of about less than 0.1% sulfuric acid may be used to simultaneously clean the wafer backside and also anneal the wafer. The circulating dilute mineral acid is pumped from a close loop reservoir and chamber arrangement. Organic mineral acid like solutions containing 0.1% or less methyl sulfonic acid may be used in the invention. Combinations or mixtures of mineral and organic acid solutions may be used in the cleaning/annealing process. Also, amino acids, hydroxides, or other suitable bases may also be used as the cleaning/annealing fluid. For example, a small amount of benzotriazole type compounds (e.g. 2 g/L) may be added to deoxygenate the fluid. A copper concentration sensor in combination with conventional electrodes closely monitors the amount of copper in the cleaning fluid. When copper concentration in the rinsing/annealing fluid exceeds some threshold, the electrodes are energized to plate by the copper ion monitor, to plate out copper metal from the cleaning fluid in the reservoir.

The invention can also be used with other applications. For example, the substrate can be a metal seed layer or base layer deposited by electrolyses, sputtering, or other methods. The present process may be used to enlarge the grain size of the seed layer prior to metal deposition. The present method can also be used to reduce oxide film of some metallic structures to enhance appearance of the substrate or to prepare the substrate prior to subsequent deposition processes. For example, cleaning and annealing a sputtered copper or gold seed layer using the process of the present

invention modifies the structure of the seed layer and, by so doing, modifies the microstructure of the subsequently plated film on the seed layer. The present method may also be used to modify the microstructure and properties of magnetic alloys, such as permalloy, and various magneto-resistive type films.

5 In addition, the present invention may be used to modify the grain size, microstructure, texture, corrosion resistance, and other metallurgical properties of the deposited metal. For example, a first metal layer may be deposited on a suitable substrate by electrochemical or non-electrochemical process for a brief period of time (e.g., 30 to 60 seconds). The first metal layer is rapidly cleaned/annealed to modify the metallurgical properties. Then, a second metal layer from the same  
10 bath or other source (e.g., chrome layer) is formed over the first metal layer. Thus, the nature and metallurgical properties of the second metal layer (i.e. grain size) which has not yet been cleaned/annealed is significantly different from the properties of the first metal layer. For example, after the cleaning/annealing process, the first deposited layer may include grains that are between 50 to 100 nm plated copper while the second deposited layer may include grains that are between 300 to  
15 2000 nm plated copper. If, on the other hand, the first metal layer is not cleaned/annealed using the process of the present invention, then the metallurgical properties of the first and second metal layers may be the same.

Because the grain size is increased as mentioned above, the corrosion resistance is also improved. Corrosion occurs generally between boundaries of the grains. When the size of the grains  
20 is increased, there are fewer boundaries within the material and thus, the corrosion resistance is improved.

In another example, copper may be used as the first deposited layer, while permalloy or chromium may be used as the second deposited layer. In other embodiments, any combination of platable materials or materials deposited by other methods can be used in the present invention. The  
25 present invention can also be used to form multi-layers of metal to form a symmetric/non-symmetric laminate film. The laminate film may contain a different type of material or non-homogeneous material. The laminate film may be given further processing treatment such as annealing at a different temperature and ambient in order to form a different alloy film or new materials. Besides using the standard deoxygenated DI water or other cleaning/annealing fluid as discussed above, various  
30 nitrogen and non-nitrogen bearing organic compounds such amines, carbonates, amides, hydroxyamines, surfactants, and their combinations may be added to the cleaning/annealing fluid to modify the surface of the cleaned/annealed material. The addition of these additives or other suitable additives to the cleaning fluid may be used to impart desirable metallurgical properties to the cleaned/annealed surface. For example, the cleaning/annealing process may impart superior  
35 corrosion resistance to the cleaned surface. It could be used to modify the cleaned material surface

such that the subsequent deposit material or materials on the cleaned surface possesses unique and beneficial material properties such as texture, grain size, magnetic and isotropic, optical, magneto-optical properties, and tribological properties.

5 The process according to the present invention is also used to alter the texture of the plated material. For example, the texture may change from a  $\langle 111 \rangle$  orientation to a  $\langle 110 \rangle$  or more random orientation after the cold working process.

10 In the previous descriptions, numerous specific details are set forth, such as specific materials, structures, chemicals, processes, etc., to provide a thorough understanding of the present invention. However, as one having ordinary skill in the art would recognize, the present invention can be practiced without resorting to the details specifically set forth.

Although only the above embodiments have been described in detail above, those skilled in the art will readily appreciate that many modifications of the exemplary embodiment are possible without materially departing from the novel teachings and advantages of this invention.

We claim:

1. A method for simultaneously cleaning and annealing a plated workpiece, the method comprising the step of applying the cleaning and annealing fluid to the workpiece while heating the workpiece and the applied cleaning and annealing fluid.

5 2. A method according to claim 1, wherein the step of heating the workpiece and the applied cleaning and annealing fluid comprises the step of energizing a heating source.

3. A method according to claim 2, wherein the heating source comprises reflectors that are adapted to reflect heat to the workpiece.

10 4. A method according to claim 1, wherein the step of heating the workpiece and the applied cleaning and annealing fluid comprises the step of generating microwave radiation.

5. A method according to claim 1, wherein the cleaning and annealing fluid comprises deoxygenated DI water.

6. A method according to claim 5 further comprising the step of deoxygenating the DI water by adding bubbled nitrogen to the DI water.

15 7. A method according to claim 5 further comprising the step of deoxygenating the DI water by adding bubbled argon to the DI water.

8. A method according to claim 5 further comprising the step of deoxygenating the DI water by adding bubbled inert gas to the DI water.

20 9. A method according to claim 5 further comprising the step of deoxygenating the DI water by adding a reducing gas such as hydrogen or forming gas.

10. A method according to claim 5 further comprising the step of deoxygenating the DI water by adding pentose sugar to the DI water.

11. A method according to claim 5, wherein the deoxygenated DI water is heated to a temperature between 60-99 °C.

25 12. A method according to claim 1 further comprising the step of rotating the workpiece about a first axis while applying the cleaning and annealing fluid.

13. A method according to claim 12, wherein the step of rotating the workpiece comprises the step of rotating the workpiece between 2-20 revolutions per minute.

30 14. A method according to claim 1, wherein the workpiece comprises one of a semiconductor wafer, flat panel, magnetic film head, and a packaging substrate.

15. A method according to claim 1, wherein the workpiece is heated to a temperature between 60 to 450 °C.

16. A method according to claim 15, wherein the workpiece is heated for 5 seconds to 20 minutes.

35 17. A method according to claim 1, wherein the cleaning and annealing fluid comprises

propylene glycol.

18 A method according to claim 17, wherein the propylene glycol is heated to a temperature between 60-200 °C.

19. A method according to claim 1, wherein heating the workpiece and the applied cleaning  
5 and annealing fluid heat the applied cleaning and annealing fluid to a temperature above the temperature at which the cleaning and annealing fluid was originally applied.

20. A method according to claim 1, wherein the cleaning and annealing fluid is heated to a temperature between 60-200 °C prior to applying the cleaning and annealing fluid to the workpiece.

21. A method for cleaning and annealing a plated workpiece, the method comprising the steps  
10 of:

rinsing the workpiece using mineral acid;

rinsing the workpiece using DI water; and

simultaneously cleaning and annealing the workpiece using heated deoxygenated DI water.

22. A method according to claim 21, wherein the step of rinsing the workpiece using mineral  
15 acid is performed after the step of rinsing the workpiece using DI water.

23. A method according to claim 21, wherein the step of simultaneously cleaning and annealing the workpiece is performed after the rinsing steps.

24. A method according to claim 21, wherein the steps of rinsing the workpiece using mineral acid and DI water are performed while rotating the workpiece.

20 25. A method according to claim 21, wherein the mineral acid comprises a solution containing 1 to 15% sulfuric acid.

26. A method according to claim 21, wherein the heated deoxygenated DI water comprises less than 0.1% sulfuric or sulfonic acids.

27. A method according to claim 21 further comprising the step of heating the deoxygenated DI  
25 water and the workpiece while simultaneously cleaning and annealing the workpiece, wherein the deoxygenated DI water is heated to a temperature between 60-99 °C.

28. A method according to claim 21, wherein the workpiece comprises one of a semiconductor wafer, flat panel, magnetic film head, and a packaging substrate.

29. A method according to claim 21 further comprising the step of heating the workpiece to a  
30 temperature between 60 to 450 °C.

30. A method according to claim 29, wherein the workpiece is heated for 5 seconds to 20 minutes.

31. A method according to claim 21 further comprising the step of deoxygenating the DI water by adding bubbled nitrogen to the DI water.

35 32. A method according to claim 21 further comprising the step of deoxygenating the DI water

by adding bubbled argon to the DI water.

33. A method according to claim 21 further comprising the step of deoxygenating the DI water by adding bubbled inert gas to the DI water.

34. A method according to claim 21 further comprising the step of deoxygenating the DI water  
5 by adding a reducing gas such as hydrogen or forming gas.

35. A method according to claim 21 further comprising the step of deoxygenating the DI water by adding pentose sugar to the DI water.

36. A method according to claim 21 further comprising the step of deoxygenating the DI water by adding additives to the DI water.

10 37. A method according to claim 21, wherein heated deoxygenated DI water is heated to a temperature between 60 to 99 °C prior to cleaning and annealing the workpiece.

38. A chamber for cleaning and annealing a plated workpiece, comprising:

a shower head having an inlet and a plurality of apertures, wherein the inlet is adapted to receive a cleaning and annealing fluid and the plurality of apertures are adapted to apply the cleaning  
15 and annealing fluid to a first face of the workpiece; and

a heating source positioned in proximity to a bottom side of the chamber and adapted to simultaneously heat the workpiece and the applied cleaning and annealing fluid.

39. The chamber according to claim 38 further comprising a means for applying the cleaning and annealing fluid to a second face of the workpiece.

20 40. The chamber according to claim 38, wherein the heating source comprises reflectors that are adapted to reflect heat to the workpiece and the cleaning and annealing cleaning fluid.

41. The chamber according to claim 38, wherein the cleaning and annealing fluid comprises deoxygenated DI water.

25 42. The chamber according to claim 41, wherein the deoxygenated DI water is heated to a temperature between 60-99 °C.

43. The chamber according to claim 38, wherein the cleaning and annealing fluid comprises propylene glycol.

44. The chamber according to claim 43, wherein the propylene glycol is heated to a temperature between 60-200 °C.

30 45. The chamber according to claim 38, wherein the workpiece comprises one of a semiconductor wafer, flat panel, magnetic film head, and a packaging substrate.

46. The chamber according to claim 38, wherein the workpiece is adapted to rotate about a first axis.

35 47. The chamber according to claim 38, wherein the applied cleaning and annealing fluid is heated to a temperature above the temperature at which the cleaning and annealing fluid was

originally applied.

48. The chamber according to claim 38, wherein the cleaning and annealing fluid is heated to a temperature between 60-200 °C prior to applying the cleaning and annealing fluid to the side of the workpiece.

5 49. A method for altering the grain size and the texture of a substrate, the method comprising the step of heating the substrate and deoxygenated DI water while applying the deoxygenated DI water to the substrate.

50. A method according to claim 49, wherein the substrate comprises a plated layer.

51. A method according to claim 49, wherein the plated layer comprises a seed layer.

10 52. A method according to claim 49, wherein the deoxygenated DI water is heated to a temperature between 60-99 °C prior to applying the deoxygenated DI water to the substrate.

53. A method according to claim 49, wherein the grain size is increased from less than 100 nm up to 2000 nm.

15 54. A method according to claim 49, wherein the texture is altered from a <111> orientation to either a <110> or a random orientation.

55. A method for altering the metallurgical properties of a first layer on a workpiece before a second layer is formed thereon on, the method comprising the steps of:

forming a first layer on a substrate;

20 simultaneously heating the workpiece and the cleaning and annealing fluid to simultaneously clean and anneal the first layer; and

forming the second layer on the first layer after the cleaning and annealing fluid has been applied to the first layer.

56. A method according to claim 55, wherein the first layer and the second metal layer comprise of same material.

25 57. A method according to claim 55, wherein the first layer and the second layer comprise of different material.

58. A method according to claim 55, wherein the cleaning and annealing fluid is heated to a temperature between 60-200 °C prior to applying the cleaning and annealing fluid to the workpiece.

30 59. A method according to claim 55, wherein the metallurgical properties comprise of a grain size, texture, magnetic, optical, magneto-optical properties, tribological properties, and corrosion resistance.

60. A method according to claim 59, wherein the grain size is increased from less than 100 nm up to 2000 nm.

35 61. A method according to claim 60, wherein the corrosion resistance is improved by decreasing grain boundaries when the grain size is increased from less than 100 nm up to 2000 nm.

62. A method according to claim 59, wherein the texture is altered from a  $\langle 111 \rangle$  orientation to either a  $\langle 110 \rangle$  or a random orientation.

63. A method according to claim 55, wherein the first and second layers comprise any adjacent layers that are formed on the substrate.

5 64. A method according to claim 55, wherein the first layer is a seed layer formed using CVD or electrolyses deposition.

65. A method according to claim 55, wherein a laminate structure of the altered first layer forms a new material.



FIG. 1 (PRIOR ART)

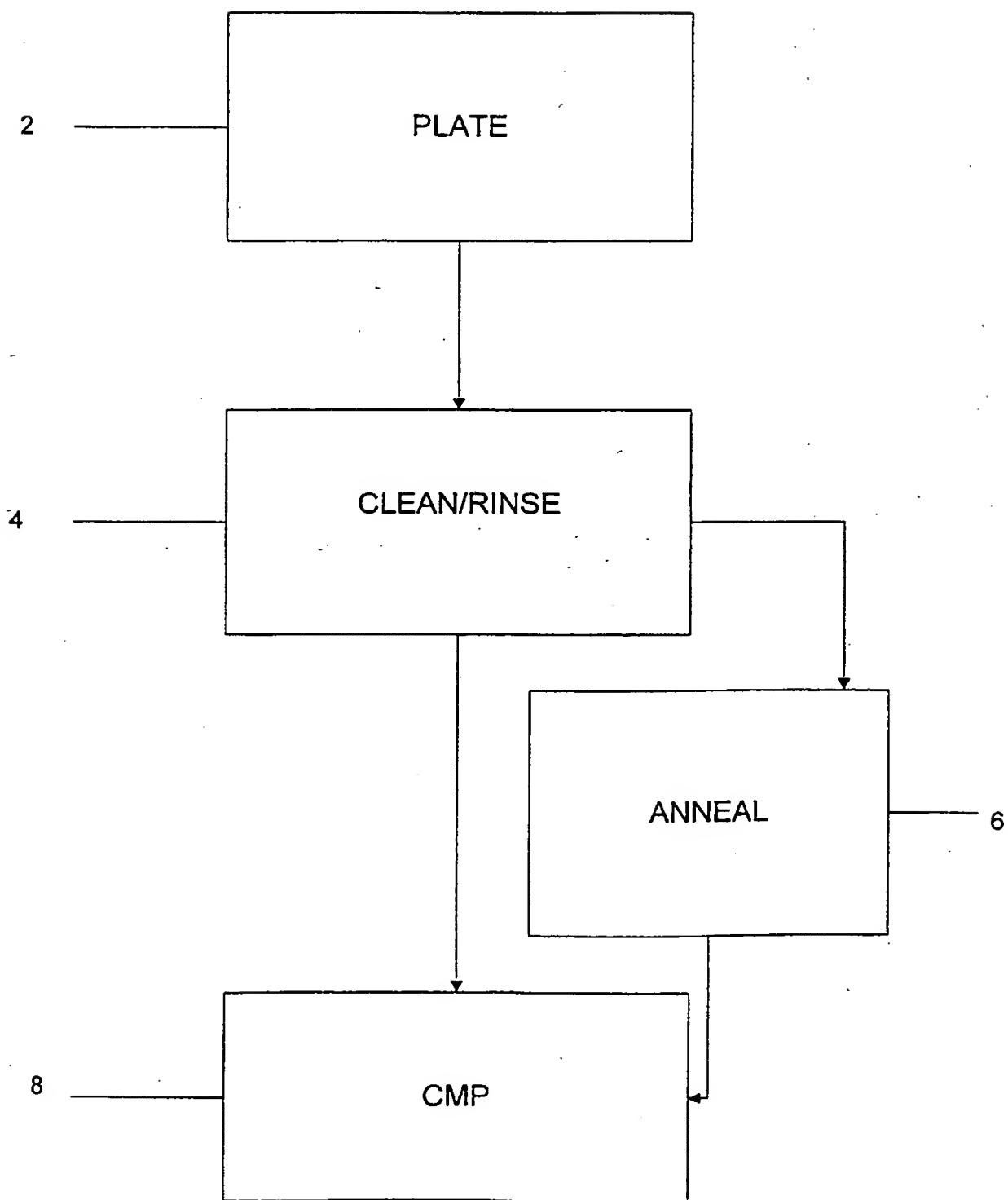
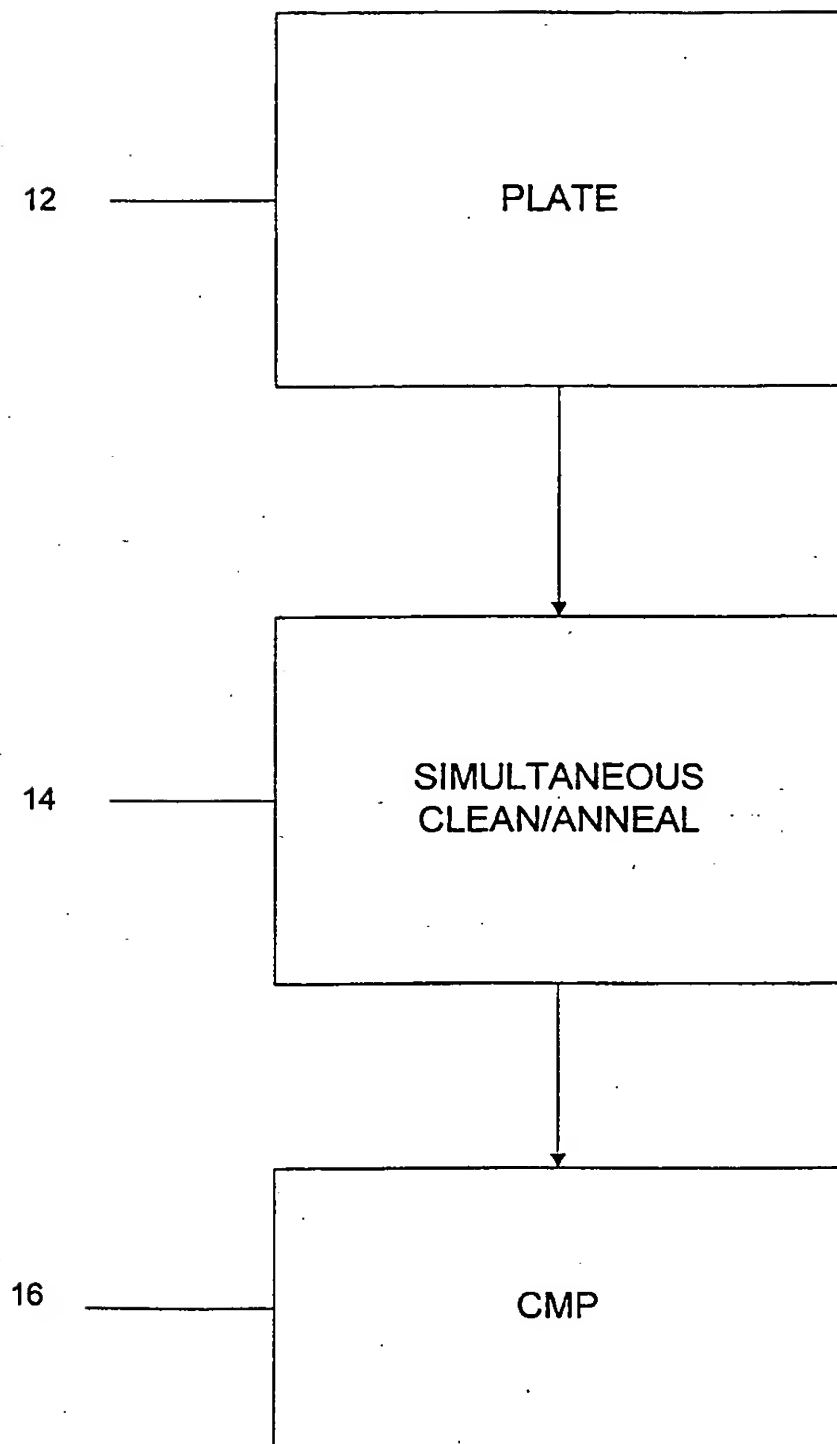


FIG. 2



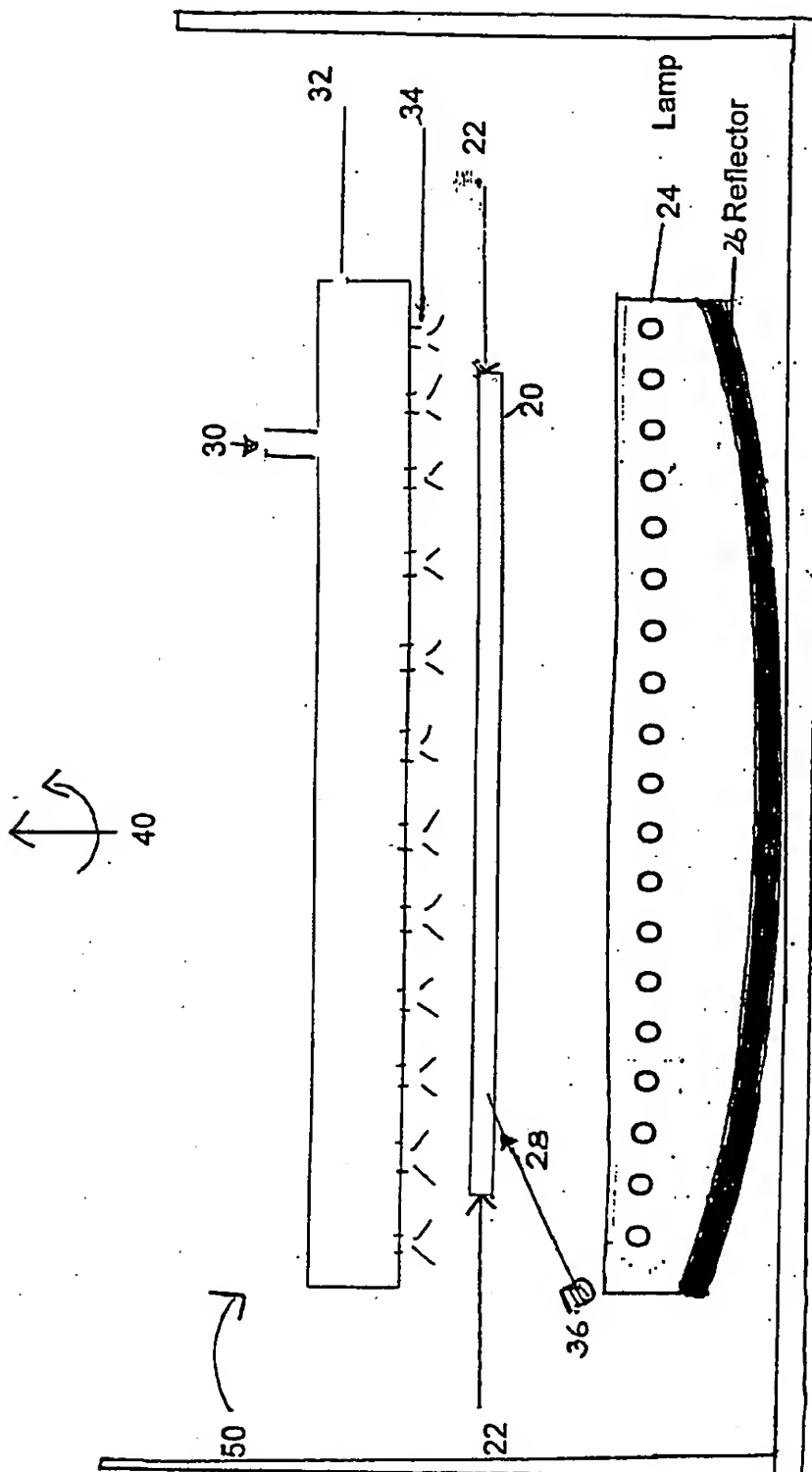


FIGURE 3

FIG. 4

